



Specifications	
<b>Number of channels</b>	8 differential
<b>Input ranges</b>	0~10 V, 1~5 V (input impedance 10 MΩ)
<b>Line break detection</b>	1~5 V
<b>Resolution</b>	16 bits
<b>Accuracy</b>	±0.1% max at 25 °C
<b>Drift</b>	<b>Zero Drift:</b> +/-0.06 μV/°C <b>Span Drift:</b> +/-30 PPM/°C
<b>Step response (5~95%)</b>	50 ms/channel
<b>Setup time</b>	20 ms/channel
<b>Settle time</b>	50 ms/channel
<b>Conversion method</b>	Sigma-Delta
<b>Rejection mode</b>	<b>Common:</b> 150 dB@60 Hz <b>Normal:</b> 150 dB@60 Hz
<b>Isolation</b>	2.5 KV optical isolation between input signals and CPU
<b>Internal current consumption</b>	400 mA
<b>Range selection</b>	DIP switches, all channels must be same range
<b>External connectors</b>	20-pt. terminal block connector, max. wire size #14 AWG
<b>Weight</b>	395 g
Features	
<ul style="list-style-type: none"> <li>• Built-in high/low limit detection capabilities</li> <li>• Hot swappable under power</li> <li>• Individual channel enable/disable</li> <li>• Engineering Unit Scaling</li> </ul>	

Terminal #	Signal	Diagram
1	CH1+	
2	CH1-	
3	CH2+	
4	CH2-	
5	CH3+	
6	CH3-	
7	CH4+	
8	CH4-	
9	GND	
10	FG	
11	CH5+	
12	CH5-	
13	CH6+	
14	CH6-	
15	CH7+	
16	CH7-	
17	CH8+	
18	CH8-	
19	GND	
20	FG	

