



Specifications	
Number of channels	8 differential
Input ranges	0~10 V, 1~5 V (input impedance 10 MΩ)
Line break detection	1~5 V
Resolution	16 bits
Accuracy	±0.1% max at 25 °C
Drift	Zero Drift: +/-0.06 μV/°C Span Drift: +/-30 PPM/°C
Step response (5~95%)	50 ms/channel
Setup time	20 ms/channel
Settle time	50 ms/channel
Conversion method	Sigma-Delta
Rejection mode	Common: 150 dB@60 Hz Normal: 150 dB@60 Hz
Isolation	2.5 KV optical isolation between input signals and CPU
Internal current consumption	400 mA
Range selection	DIP switches, all channels must be same range
External connectors	20-pt. terminal block connector, max. wire size #14 AWG
Weight	395 g
Features	
<ul style="list-style-type: none"> • Built-in high/low limit detection capabilities • Hot swappable under power • Individual channel enable/disable • Engineering Unit Scaling 	

